

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

First Named Inventor: Chang  
Application Number: 10/823,513  
Filing Date: April 12, 2004  
Title: Autorun for Integrated Circuit Memory Component

Examiner: Rampuria, Satish  
Art Unit: 2191

**CORRECTED AMENDMENT**

Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Applicants respond as follows to the Patent and Trademark Office action  
dated July 10, 2008: